| | Type | Hit | Search Text | DBs | Time Stamp |
|---|---|---|---|---|---|
| 1 | BRS | 24 | "6635918" "6150689" | USPAT; US-PGPUB | 2004/08/25 |
| 2 | BRS | 0 | (257/306.ccls.) and (@ad<1997014) | USPAT; US-PGPUB | 2004/08/25 |
| 3 | BRS | 1 | "4937645".PN. | USPAT | 2004/08/25 |
| 4 | BRS | 12 | ("4937645"|"5188975"|"5204286"|"5240505"|"5264712"|"5336638"|"5471079"|"5567864"|"5760475"|"5793076"|"578347 | USPAT; US-PGPUB | 2004/08/25 |
| 5 | BRS | 2 | ((( ( (bit$1line or (bit adj line) or (b adj l) or b$1l) with (second adj (dielectric or insulating or insulator or insulation)) )) an | USPAT; US-PGPUB | 2004/08/25 |
| 6 | BRS | 0 | ((( ( (bit$1line or (bit adj line) or (b adj l) or b$1l) with (second adj (dielectric or insulating or insulator or insulation)) )) an | USPAT; US-PGPUB | 2004/08/25 |
| 7 | BRS | 13 | ((( ( (bit$1line or (bit adj line) or (b adj l) or b$1l) with (second adj (dielectric or insulating or insulator or insulation)) )) an | USPAT; US-PGPUB | 2004/08/25 |
| 8 | BRS | 22 | (((( (bit$1line or (bit adj line) or (b adj l) or b$1l) and (dram or (dynamic adj (ram or random)) ) and (@ad<19960113) an | USPAT; US-PGPUB | 2004/08/25 |
| 9 | BRS | 12 | (US-6548852-$ or US-6281540-$ or US-6130449-$ or US-6104053-$ or US-5981369-$ or US-5414655-$ or US-625568 | USPAT; US-PGPUB | 2004/08/25 |
| 10 | BRS | 3 | (((US-6548852-$ or US-6281540-$ or US-6130449-$ or US-6104053-$ or US-5981369-$ or US-5414655-$ or US-6255 | USPAT; US-PGPUB | 2004/08/25 |
| 11 | BRS | 1 | "4796228".PN. | USPAT | 2004/08/30 |
| 12 | BRS | 1 | "4979004".PN. | USPAT | 2004/08/30 |
| 13 | BRS | 1 | "5042008".PN. | USPAT | 2004/08/30 |
| 14 | BRS | 1 | "5146426".PN. | USPAT | 2004/08/30 |
| 15 | BRS | 1 | "5250458".PN. | USPAT | 2004/08/30 |
| 16 | BRS | 1 | "5309008".PN. | USPAT | 2004/08/30 |
| 17 | BRS | 16 | ((plug with electrode) and (peripheral with (cell or array or memory)) and ( (dram or (dynamic adj (ram or random)) ).ab. | USPAT; US-PGPUB | 2004/08/30 |
| 18 | BRS | 18 | ((plug with electrode) and ((peripheral or logic) with (cell or array or memory)) and ( (dram or (dynamic adj (ram or rand | USPAT; US-PGPUB | 2004/08/30 |
| 19 | BRS | 2 | (((plug with electrode) and ((peripheral or logic) with (cell or array or memory)) and ( (dram or (dynamic adj (ram or rand | USPAT; US-PGPUB | 2004/08/30 |
| 20 | BRS | 17 | ((barrier with hole) and ((peripheral or logic) with (cell or array or memory)) and ( (dram or (dynamic adj (ram or random | USPAT; US-PGPUB | 2004/08/30 |
| 21 | BRS | 14 | (((barrier with hole) and ((peripheral or logic) with (cell or array or memory)) and ( (dram or (dynamic adj (ram or rando | USPAT; US-PGPUB | 2004/08/30 |
| 22 | BRS | 19 | (((titanium adj nitride) and ((peripheral or logic) with (cell or array or memory)) and ( (dram or (dynamic adj (ram or rand | USPAT; US-PGPUB | 2004/08/30 |
| 23 | BRS | 27 | ((titanium adj nitride) and ((peripheral or logic) with (cell or array or memory)) and ( (dram or (dynamic adj (ram or rando | USPAT; US-PGPUB | 2004/08/30 |
| 24 | BRS | 0 | ((plug with ((second or 2nd) adj (metal or wire or wiring or metallization)) ) and ((peripheral or logic) with (cell or array or | USPAT; US-PGPUB | 2004/08/30 |
| 25 | BRS | 5 | (ohno.in.) and (mitsubishi.as.) and (@ad<19960113) and ((257/$3.cor.) or (438/$3.cor.) or (365/$3.cor.)) | USPAT; US-PGPUB | 2004/08/30 |
| 26 | BRS | 23 | ( (dram or (dynamic adj (ram or random)) ).ab.) and (nec.as.) and (@ad<19960113) and ((257/$3.cor.) or (438/$3.co | USPAT; US-PGPUB | 2004/08/30 |
| 27 | BRS | 2 | ( (dram or (dynamic adj (ram or random)) ).ab.) and (planarized) and (@ad<19960113) and ((257/$3.cor.) or (438/$3 | USPAT; US-PGPUB | 2004/08/30 |
| 28 | BRS | 2 | ( (dram or (dynamic adj (ram or random)) ).ab.) and (planarization) and (@ad<19960113) and ((257/$3.cor.) or (438/ | USPAT; US-PGPUB | 2004/08/30 |
| 29 | BRS | 20 | ( (dram or (dynamic adj (ram or random)) ).ab.) and (@ad<19960113) and ((257/$3.cor.) or (438/$3.cor.) ) and ((thi | USPAT; US-PGPUB | 2004/08/30 |
| 30 | BRS | 4 | ( (dram or (dynamic adj (ram or random)) )) and (@ad<19960113) and ((257/$3.cor.) or (438/$3.cor.) ) and (((third | USPAT; US-PGPUB | 2004/08/30 |
| 31 | BRS | 21 | ( (dram or (dynamic adj (ram or random)) )) and (@ad<19960113) and ((257/$3.cor.) or (438/$3.cor.) ) and (((secon | USPAT; US-PGPUB | 2004/08/31 |
| 32 | BRS | 2 | ( (dram or (dynamic adj (ram or random)) )) and (@pd<19960113) and (((second or 2nd) adj (metal or wire or wiring | EPO; JPO; DERWENT | 2004/08/31 |
| 33 | BRS | 4 | ( (dram or (dynamic adj (ram or random)) )) and (@pd<19960113) and (barrier adj film) | EPO; JPO; DERWENT | 2004/08/31 |
| 34 | BRS | 20 | ( (dram or (dynamic adj (ram or random)) )) and (@ad<19960113) and (barrier adj film) | USPAT; US-PGPUB | 2004/08/31 |
| 35 | BRS | 1 | "5569618".PN. | USPAT | 2004/08/31 |
| 36 | BRS | 1 | "5508221".PN. | USPAT | 2004/08/31 |
| 37 | BRS | 1 | "5429979".PN. | USPAT | 2004/08/31 |
| 38 | BRS | 1 | "5346843".PN. | USPAT | 2004/08/31 |
| 39 | BRS | 1 | "5302549".PN. | USPAT | 2004/08/31 |
| 40 | BRS | 21 | (plug with ((second or 2nd) adj (metal or wire or wiring or metallization)) ) and ((peripheral or logic) with (cell or array or | USPAT; US-PGPUB | 2004/08/31 |
| 41 | BRS | 21 | (( (tin or (titanium adj nitride)) with (w or tungsten) ) and (@ad<19960113) and ((dram or (dynamic adj (ram or rando | USPAT; US-PGPUB | 2004/08/31 |
| 42 | BRS | 7 | (( (tin or (titanium adj nitride)) with (w or tungsten) ) and (@ad<19960113) and memory and ( (bit$1line or (bit adj line) o | USPAT; US-PGPUB | 2004/08/31 |
| 43 | BRS | 19 | (257/296.cor. ) and (@ad<19960113) and memory and ( (bit$1line or (bit adj line) or (b adj l) or b$1l) with ((first or 1st | USPAT; US-PGPUB | 2004/08/31 |

| # | Type | Hit | Search Text | DBs | Time Stamp |
|---|------|-----|-------------|-----|------------|
| 19 | BRS | 2 | (((plug with electrode) and ((peripheral or logic) with (cell or array or memory)) and ( (dram or (dynamic adj (ram or rand | USPAT; US-PGPUB | 2004/08/30 |
| 20 | BRS | 17 | ((barrier with hole) and ((peripheral or logic) with (cell or array or memory)) and ( (dram or (dynamic adj (ram or random | USPAT; US-PGPUB | 2004/08/30 |
| 21 | BRS | 14 | (((barrier with hole) and ((peripheral or logic) with (cell or array or memory)) and ( (dram or (dynamic adj (ram or rando | USPAT; US-PGPUB | 2004/08/30 |
| 22 | BRS | 19 | (((titanium adj nitride) and ((peripheral or logic) with (cell or array or memory)) and ( (dram or (dynamic adj (ram or rand | USPAT; US-PGPUB | 2004/08/30 |
| 23 | BRS | 27 | ((titanium adj nitride) and ((peripheral or logic) with (cell or array or memory)) and ( (dram or (dynamic adj (ram or rando | USPAT; US-PGPUB | 2004/08/30 |
| 24 | BRS | 0 | ((plug with ((second or 2nd) adj (metal or wire or wiring or metallization)) ) and ((peripheral or logic) with (cell or array or | USPAT; US-PGPUB | 2004/08/30 |
| 25 | BRS | 5 | (ohno.in.) and (mitsubishi.as.)  and (@ad<19960113) and ((257/$3.cor.) or (438/$3.cor.) or (365/$3.cor.)) | USPAT; US-PGPUB | 2004/08/30 |
| 26 | BRS | 23 | ( (dram or (dynamic adj (ram or random)) ).ab.)  and (nec.as.)  and (@ad<19960113) and ((257/$3.cor.) or (438/$3.co | USPAT; US-PGPUB | 2004/08/30 |
| 27 | BRS | 2 | ( (dram or (dynamic adj (ram or random)) ).ab.)  and (planarized)  and (@ad<19960113) and ((257/$3.cor.) or (438/$3 | USPAT; US-PGPUB | 2004/08/30 |
| 28 | BRS | 2 | ( (dram or (dynamic adj (ram or random)) ).ab.)  and (planarization)  and (@ad<19960113) and ((257/$3.cor.) or (438/ | USPAT; US-PGPUB | 2004/08/30 |
| 29 | BRS | 20 | ( (dram or (dynamic adj (ram or random)) ).ab.)    and (@ad<19960113) and ((257/$3.cor.) or (438/$3.cor.) ) and ((thi | USPAT; US-PGPUB | 2004/08/30 |
| 30 | BRS | 4 | ( (dram or (dynamic adj (ram or random)) ))    and (@ad<19960113) and ((257/$3.cor.) or (438/$3.cor.) ) and (((third | USPAT; US-PGPUB | 2004/08/30 |
| 31 | BRS | 21 | ( (dram or (dynamic adj (ram or random)) ))    and (@ad<19960113) and ((257/$3.cor.) or (438/$3.cor.) ) and (((secon | USPAT; US-PGPUB | 2004/08/31 |
| 32 | BRS | 2 | ( (dram or (dynamic adj (ram or random)) ))    and (@ad<19960113) and (((second or 2nd) adj (metal or wire or wiring | EPO; JPO; DERWENT | 2004/08/31 |
| 33 | BRS | 4 | ( (dram or (dynamic adj (ram or random)) ))    and (@pd<19960113) and (barrier adj film) | EPO; JPO; DERWENT | 2004/08/31 |
| 34 | BRS | 20 | ( (dram or (dynamic adj (ram or random)) ))    and (@ad<19960113) and (barrier adj film) | USPAT; US-PGPUB | 2004/08/31 |
| 35 | BRS | 1 | "5569618".PN. | USPAT | 2004/08/31 |
| 36 | BRS | 1 | "5508221".PN. | USPAT | 2004/08/31 |
| 37 | BRS | 1 | "5429979".PN. | USPAT | 2004/08/31 |
| 38 | BRS | 1 | "5346843".PN. | USPAT | 2004/08/31 |
| 39 | BRS | 1 | "5302549".PN. | USPAT | 2004/08/31 |
| 40 | BRS | 21 | (plug with ((second or 2nd) adj (metal or wire or wiring or metallization)) ) and ((peripheral or logic) with (cell or array or | USPAT; US-PGPUB | 2004/08/31 |
| 41 | BRS | 21 | ( (( tin or (titanium adj nitride)) with (w or tungsten) )    and (@ad<19960113) and  ((dram or (dynamic adj (ram or rando | USPAT; US-PGPUB | 2004/08/31 |
| 42 | BRS | 7 | ( (tin or (titanium adj nitride)) with (w or tungsten) )    and (@ad<19960113) and memory and ( (bit$1line or (bit adj line) o | USPAT; US-PGPUB | 2004/08/31 |
| 43 | BRS | 19 | (257/296.cor. )    and (@ad<19960113) and memory and ( (bit$1line or (bit adj line) or (b adj l) or b$1l) with ((first or 1st | USPAT; US-PGPUB | 2004/08/31 |
| 44 | BRS | 10 | ((257/296.ccls. )    and (@ad<19960113) and memory and ( (bit$1line or (bit adj line) or (b adj l) or b$1l) with ((first or 1 | USPAT; US-PGPUB | 2004/09/04 |
| 45 | BRS | 31 | (((plug with electrode) and  (dram or (dynamic adj (ram or random)) ).ab.)    and (@ad<19960113) ) not (trench.ab.) | USPAT; US-PGPUB | 2004/09/04 |
| 46 | BRS | 8 | (plug with electrode) and  (dram or (dynamic adj (ram or random)) ) and (international.as.) and (@ad<19960113)  not (tr | USPAT; US-PGPUB | 2004/09/04 |
| 47 | BRS | 19 | ((plug with electrode) and  (dram or (dynamic adj (ram or random)) )  and (@ad<19960113)  not (trench.ab.)) and (dram | USPAT; US-PGPUB | 2004/09/04 |
| 48 | BRS | 29 | (257/296.ccls. )    and (@ad<19960113) and memory and ( (bit$1line or (bit adj line) or (b adj l) or b$1l) with ((first or 1st | USPAT; US-PGPUB | 2004/09/04 |
| 49 | BRS | 31 | (((plug with electrode) and  (dram or (dynamic adj (ram or random)) ).ab.)    and (@ad<19960113) ) not (trench.ab.) | USPAT; US-PGPUB | 2004/09/04 |
| 50 | BRS | 0 | (((stud with electrode) and  (dram or (dynamic adj (ram or random)) ).ab.)    and (@ad<19960113) ) not (trench.ab.) | USPAT; US-PGPUB | 2004/09/04 |
| 51 | BRS | 0 | (((stud with electrode) and  (dram or (dynamic adj (ram or random)) ).ab.)    and (@ad<19960113) ) not (trench.ab.) | USPAT; US-PGPUB | 2004/09/04 |
| 52 | BRS | 3 | ((US-6548852-$ or US-6281540-$ or US-6130449-$ or US-6104053-$ or US-5981369-$ or US-5414655-$ or US-62556 | USPAT; US-PGPUB | 2004/09/04 |
| 53 | BRS | 13 | (( "4937645" | USPAT; US-PGPUB | 2004/09/04 |
| 54 | BRS | 12 | (( "4937645" | USPAT; US-PGPUB | 2004/09/04 |
| 55 | BRS | 1 | "4873559".PN. | USPAT | 2004/09/04 |
| 56 | BRS | 1 | "5235199".PN. | USPAT | 2004/09/04 |
| 57 | BRS | 5 | (((plug with electrode) and  (dram or (dynamic adj (ram or random)) )  and (@ad<19960113)  not (trench.ab.)) and (dra | USPAT; US-PGPUB | 2004/09/05 |
| 58 | BRS | 20 | (US-6548852-$ or US-6104053-$ or US-6281540-$ or US-5414655-$ or US-5981369-$ or US-6130449-$ or US-625568 | USPAT; US-PGPUB | 2004/09/06 |
| 59 | BRS | 4 | ((US-6548852-$ or US-6104053-$ or US-6281540-$ or US-5414655-$ or US-5981369-$ or US-6130449-$ or US-62556 | USPAT; US-PGPUB | 2004/09/06 |
| 60 | BRS | 5 | "07106437" "08053494" "64027243" | JPO; DERWENT | 2004/09/06 |